The International Symposium on Computer Architecture is the premier forum for new ideas and experimental results in computer architecture. This year’s conference will be held in the amazing, beautiful Saint-Malo, France.

Novel papers are solicited on a broad range of topics, including, but not limited to:

- Processor, memory, and storage systems architecture
- Parallel and multi-core systems and Interconnection networks
- Instruction, thread, and data-level parallelism
- Architecture support for programming productivity, debugging & correctness, reliability & fault tolerance, security, and virtualization
- Power and energy efficient architectures
- Application specific, reconfigurable, and embedded architectures
- Network processor and router architectures
- Architectures for emerging technologies and applications
- Effect of circuits and technology on architecture
- Architecture modeling and simulation methodology
- Performance evaluation and measurement of real systems
- Architecture and analysis of emerging computing platforms, such as mobile devices, game consoles, and datacenter-scale computing

We also encourage papers describing forward looking, Qualitative Out of the Box, ideas (QOB). These QOB papers (marked by the authors as such) will get separate PC attention: reviews will take into account the qualitative, creativity and novelty flavors of these papers. The QOB papers may be shorter than the regular paper and may not always be subjected to the rigorous quantitative evaluations and comparisons that are possible in more established areas.

Call For Papers

IMPORTANT DATES:

Abstract Deadline: November 9th, 2009, 1:00PM PST
Final Paper Deadline: November 16th, 2009, 1:00PM PST

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